

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	365	hardware with (context\$3 adj switch\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 16:54
L2	236	cache and 1	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 16:54
L3	223	register\$1 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 16:59
L4	22805	(code or instruction) adj (memory or storage or buffer or ram)	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 16:59
L5	60	3 and 4	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 17:55
L6	4	hardware adj assist\$3 near3 context near2 switch\$3	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 18:03
L7	11180	time adj critical	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 18:04
L8	459	time adj critical adj (task\$1 or thread\$1 or process\$2)	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 18:04
L9	0	6 and 8	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 18:05
L10	6993	reduc\$4 adj3 sav\$3	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 18:05
L11	1	8 and 10	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 18:06
L12	575	712/228.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 18:07
L13	21	10 and 12	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 18:07
L14	9	8 and 12	US-PGPUB; USPAT; EPO	OR	OFF	2006/01/18 18:07